

tentative

Туре	Ag <sup>*</sup> Aİ <sup>*</sup>	V <sub>DRM</sub> / V <sub>RRM</sub>	I <sub>F(AV)</sub> [A]		<b>Size</b> x [mm]	Package Options		<u> </u>
DWPJ 1	2 🗸	1600	20	3.50	3.50	sawn on foil unsawn wafer in waffle pack		
	*Frontside options					*Please contact IXYS chip sales		

# **Mechanical Parameters**

6.25 mm<sup>2</sup> Area active Area total 12.25 mm<sup>2</sup> Wafer size Ø 150 mm **Thickness** 265 μm Material Si Max. possible chips per wafer tbd Passivation front side Glassivation solderable: Al / Ti / Ni / Ag Metallization top side top side bondable: Metallization backside solderable (only): Al / Ti / Ni / Ag Recom. wire bonds (AI) Number 380 μm Reject Ink Dot Size Ø 0.4-1.0 mm Recom. Storage Environment sawn on foil in org. container, in dry nitrogen month unsawn wafer in org. container, in dry nitrogen < 2 year in waffle pack in org. container, in dry nitrogen < 2 year Τ -40 ... 40 °C

#### **Features**

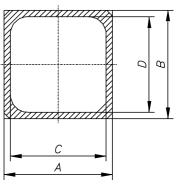
- advanced planar technology
- anode top
- glassivation
- soft recovery rectifier diode
- high commutation robustness

# **Applications**

- DC Power Supplies
- Field Supply for DC motors
- Battery DC Power Supplies
- Power Rectifiers
- Input rectifier

## **Dimensions**

A	B	C	D		
[mm]	[mm]	[mm]	[mm]		
3.50	3.50	2.50	2.50		
Anoc	de (me	tal)	$\overline{}$		Passivati
////	// //		······	<u> </u>	7///
Cath	ode (r	netal)			



IXYS reserve the right to change limits, conditions and dimensions

<sup>\*</sup>Sinterable top/bottom side on request

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Electrical p	arameters
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Symbol		Conditions				Ratings			
			m	nin.	typ.	max.			
$V_D / V$	/ <sub>R</sub>	$T_{VJ} = 25^{\circ}C$	10	600			V		
I <sub>R</sub>		$V_R = V_{RRM}$	$T_{VJ} = 25^{\circ}C$			20	μΑ		
		$V_R = 0.8 \cdot V_{RRM}$	$T_{VJ} = 150$ °C			1.5	mΑ		
V <sub>F</sub>		I <sub>F</sub> = 20 A	$T_{VJ} = 25^{\circ}C$			1.23	V		
			$T_{VJ} = 150$ °C			1.17	V		
$V_{F0}$		For power-los	s calculations only			٧			
r <sub>F</sub>		$T_{VJ} = 150$ °C					mΩ		
di/dt "	ıln	$T_{VJ} = 25^{\circ}C$	$U_{dc} = 600V$			100	A/μs		
		$T_{VJ} = 150$ °C	$U_{dc} = 600V$			100	A/μs		
$T_{VJ}$				-40		150	°C		
I <sub>F(AV)</sub>	*	T <sub>C</sub> = 100 °C	180° rect. T <sub>vJ</sub> = 150°C		20		Α		
I <sub>FSM</sub>	*	$T_{VJ} = 45^{\circ}C$	t = 10 ms (50) Hz, sine			350	Α		
		$V_R = 0 V$	t = 8.3  ms  (60)  Hz, sine			340	Α		
		$T_{VJ} = 150^{\circ}C$	t = 10 ms (50) Hz, sine			280	Α		
		$V_R = 0 V$	t = 8.3  ms  (60)  Hz, sine			270	Α		
I² t	*	$T_{VJ} = 45^{\circ}C$	t = 10 ms (50) Hz, sine			610	A s <sup>2</sup>		
		$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			480	A s		
		$T_{VJ} = 150$ °C	t = 10 ms (50) Hz, sine			390	A s <sup>2</sup>		
		$V_R = 0 V$	t = 8.3  ms  (60)  Hz, sine			300	$A s^2$		
R <sub>thJC</sub>	*	DC current				1.80	K/W		

<sup>\*</sup> Data according to assembled Chip

VHFD (bondable)

Data according to IEC 60747

## Terms of Conditions and Usage

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Should you intend to use the product in aviation applications, in health or life endangering or life support applications, please notify. For any such applications we urgently recommend

- to perform joint risk and quality assessments;
- the conclusion of quality agreements;
- to establish joint measures to ensure application specific product capabilities and notify that IXYS may delivery dependent on the realization of any such measures.

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