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Туре	V <sub>RRM</sub>	I <sub>F(AV)</sub> [A]	Chip Size [mm] x [mm]	Package Options	• <del>       </del> •
DWPJ 154-16 AL	1600	270	12.4 12.4	sawn on foil  in waffle pack	

# **Mechanical Parameters**

132 mm<sup>2</sup> Area active mm<sup>2</sup> Area total 153.76 Wafer size Ø 150 mm **Thickness** 265 μm Material Si tbd Max. possible chips per wafer Passivation front side Glassivation Metallization top side bondable: Metallization backside solderable (only): Al / Ti / Ni / Ag Recom. wire bonds (AI) Number 10 Ø 380 μm Ø 0.4-1.0 mm Reject Ink Dot Size Recom. Storage Environment

sawn on foil in org. container, in dry nitrogen < 6 months in org. container, in dry nitrogen < 2 unsawn wafer years in waffle pack in org. container, in dry nitrogen < 2 years Recom. storage temperature -40 ... 40 °C

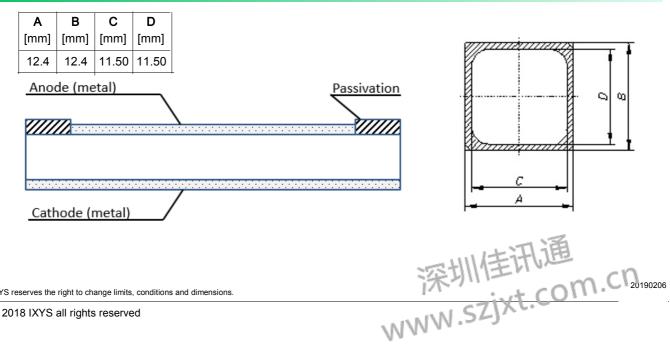
#### **Features**

- advanced planar technology
- anode top
- glassivation
- soft recovery rectifier diode
- high commutation robustness

### **Applications**

- DC power supplies
- field supply for DC motors
- battery DC power supplies
- power rectifiers
- input rectifier

# **Dimensions**



IXYS reserves the right to change limits, conditions and dimensions.





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Electrica	al parameters					
Symbol	Conditions			Ratings		
			min.	typ.	max.	
$V_{RRM}$	$T_{VJ} = 25^{\circ}C$		1600			V
I <sub>R</sub>	$V_R = V_{RRM}$	$T_{VJ} = 25^{\circ}C$			300	μΑ
	$V_R = 0.8 \cdot V_{RRM}$				1	mΑ
V <sub>F</sub>	I <sub>F</sub> = 380 A			1.10		V
		$T_{VJ} = 150$ °C		1.04		V
V <sub>F0, max</sub>	Maximum forw	vard voltage range			0.90	V
r <sub>F, max</sub>	$T_{VJ}$ = 25 °C	$0.5 \cdot I_{F(AV)} < I_F < 2 \cdot I_{F(AV)}$			0.7	mΩ
di/dt	$T_{VJ} = 25^{\circ}C$	$V_{DC} = 600V$ $I_F = 2 \cdot I_{F(AV)}$ $L_{S, max} = 1.3 \mu H$ $V_{R, max} = 850 V$			tbd	A/µs
	T <sub>VJ</sub> = 150°C	$V_{DC} = 600V$ $I_{F} = 2 \cdot I_{F(AV)}$ $L_{S, max} = 1.3 \ \mu H$ $V_{R, max} = 850 \ V$			tbd	A/µs
T <sub>VJ</sub>			-40		150	°C
I <sub>F(AV)</sub> *	T <sub>c</sub> = 100 °C	180° rect. T <sub>vJ</sub> = 150°C		380		Α
I <sub>FSM</sub> *	$T_{VJ} = 25^{\circ}C$	t = 10 ms (50) Hz, sine			3750	Α
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			3500	Α
	T <sub>VJ</sub> = 150°C	t = 10 ms (50) Hz, sine			2900	Α
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			2700	Α
²t *	$T_{VJ} = 25^{\circ}C$	t = 10 ms (50) Hz, sine			70310	$A^2s$
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			51040	$A^2s$
	T <sub>VJ</sub> = 150°C	t = 10 ms (50) Hz, sine			42050	$A^2s$
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			30370	$A^2s$
R <sub>thJC</sub> *	DC current				0.15	K/W
* Data according to assembled Chip VHFD (bondable)					ccording to IE	
$V_{br}$	$T_{VJ} = 25^{\circ}C$		1740			V
	$T_{VJ} = 150^{\circ}C$		1800			V
I <sub>RSM</sub>	Avalanche cap		5	mΑ		

## Terms of Conditions and Usage

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of system characteristics when assembled. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of our product, please contact your responsible sales office.

Should you intend to use the product in aviation applications, in health or life endangering or life support applications, please notify. For any such applications we urgently recommend

- to perform joint risk and quality assessments;
- the conclusion of quality agreements;
- to establish joint measures to ensure application specific product capabilities and notify that IXYS may delivery dependent on the realization of any such measures.

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