

Туре	V_{RRM}	I _{F(AV)} [A]	Chip Size [mm] x [mm]	Package Options	• •
DWPJ 31-16 AL	1600	40	5.6 5.6	sawn on foil ✓ in waffle pack ✓	

Mechanical Parameters

Area active	21.16			mm ²
Area total			31.36	mm ²
Wafer size Ø			150	mm
Thickness			265	μm
Material			Si	
Max. possible chips per wafer			426	
Passivation front side		Glas	ssivation	
Metallization top side	bondable:		Al	
Metallization backside	solderable (only):	Al / Ti	/ Ni / Ag	*
Recom. wire bonds (AI)	1	Number	6	
		Ø	380	μm
Reject Ink Dot Size		Ø	0.4-1.0	mm
Recom. Storage Environment				
sawn on foil	in org. container, in dry	nitroge	en < 6	months

in org. container, in dry nitrogen < 2 years in org. container, in dry nitrogen < 2 years -40 ... 40 °C

Features

- advanced planar technology
- anode top
- glassivation
- soft recovery rectifier diode
- high commutation robustness

Applications

- DC power supplies
- field supply for DC motors
- battery DC power suppliespower rectifiers
- input rectifier

Dimensions

Recom. storage temperature

[mm]	[mm]	[mm]	[mm]				
5.6	5.6	4.60	4.60				
Ano	Anode (metal) Passivation						
	//						

IXYS reserves the right to change limits, conditions and dimensions.

unsawn wafer

in waffle pack



Cathode (metal)



0 1 1	0		_			
Symbol	Conditions		1	atings		
		n	nin.	typ.	max.	
V _{RRM}	$T_{VJ} = 25^{\circ}C$	1	600			V
I _R	$V_R = V_{RRM}$	$T_{VJ} = 25^{\circ}C$			150	μΑ
	$V_R = 0.8 \cdot V_{RRM}$	$T_{VJ} = 150$ °C			1	mΑ
V _F	I _F = 40 A			1.02		V
		$T_{VJ} = 150$ °C		0.92		V
V _{F0, max}	Maximum forw	ard voltage range			1.00	V
r _{F, max}	T_{VJ} = 25 °C	$0.5 \cdot I_{F(AV)} < I_F < 2 \cdot I_{F(AV)}$			2.4	$\text{m}\Omega$
di/dt	T _{VJ} = 25°C	$V_{DC} = 600V$ $I_F = 2 \cdot I_{F(AV)}$ $L_{S, max} = 1.3 \mu H$ $V_{R, max} = 850 V$			200	A/µs
	$T_{VJ} = 150^{\circ}C$	$V_{DC} = 600V$ $I_F = 2 \cdot I_{F(AV)}$ $L_{S, max} = 1.3 \ \mu H$ $V_{R, max} = 850 \ V$			200	A/µs
T _{VJ}			-40		150	°C
I _{F(AV)} *	$T_c = 100 ^{\circ}C$	180° rect. $T_{VJ} = 150$ °C		40		Α
I _{FSM} *	$T_{VJ} = 25^{\circ}C$	t = 10 ms (50) Hz, sine			750	Α
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			700	Α
	T _{vJ} = 150°C	t = 10 ms (50) Hz, sine			600	Α
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			580	Α
l²t *	T _{VJ} = 25°C	t = 10 ms (50) Hz, sine			2810	A^2s
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			2040	A^2s
	T _{vJ} = 150°C	t = 10 ms (50) Hz, sine			1800	A ² s
	$V_R = 0 V$	t = 8.3 ms (60) Hz, sine			1400	A^2s
R _{thJC} *	DC current				0.65	K/W
* Data accordi	ng to assembled Chip	VHFD (bondable)		Data a	ccording to IEC	C 60747
V_{br}	$T_{VJ} = 25^{\circ}C$		1740			V
	$T_{VJ} = 150^{\circ}C$	1	1800			V
I _{RSM}	Avalanche cap	pability			5	mΑ

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Should you intend to use the product in aviation applications, in health or life endangering or life support applications, please notify. For any such applications we urgently recommend

- to perform joint risk and quality assessments;
- the conclusion of quality agreements;
- to establish joint measures to ensure application specific product capabilities and notify that IXYS may delivery dependent on the realization of any such measures.

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