

Туре	Ag* Al*	V _{DRM} / V _{RRM}	I _{F(AV)} [A]	Chip Size [mm] x [mm]	Package Options	
DWP 1	7 🗸	1800	31	4.45 4.45	sawn on foil unsawn wafer in waffle pack	
	*Frontside options				*Please contact IXYS chip sales	

Mechanical Parameters

Area active			9.53	mm ²
Area total			19.80	mm ²
Wafer size Ø			150	mm
Thickness			265	μm
Material			Si	$X \wedge$
Max. possible chips per wafer			688	. 7
Passivation front side		Glas	sivation	
Metallization top side	solderable:	Al / Ti /	'Ni / Ag	*
top side	bondable:		Al	
Metallization backside	solderable (only):	Al / Ti /	'Ni / Ag	*
Recom. wire bonds (AI)	N	lumber	4	
		Ø	380	μm
Reject Ink Dot Size		Ø	0.4-1.0	mm
Recom. Storage Environment				
sawn on foil	in org. container, in dry	nitroger	n < 6	month
unsawn wafer	in org. container, in dry	nitroger	n < 2	year
in waffle pack	in org. container, in dry	nitroger	n < 2	year
	Т	-40	. 40	°C

Features

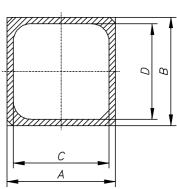
- planar technology
- anode top
- glassivation
- soft recovery rectifier diode

Applications

- DC Power Supplies
- Field Supply for DC motorsBattery DC Power Supplies
- Power Rectifiers

Dimensions

A	В	С	D	
[mm]	[mm]	[mm]	[mm]	
4.45	4.45	3.05	3.05	
Ano	de (me	tal)	$\overline{}$	Passivation
////	7		\	
	//			
				000000000000000000000000000000000000000
Cath	ode (r	netal)	/	/



^{*}Sinterable top/bottom side on request



Elec	trica	l parameters	3							
Symb	ool	Conditions			Ratings					
					min.	typ.	max.			
V _D / V	V _R	$T_{VJ} = 25^{\circ}C$			1800			V		
I_R		$V_R = V_{RRM}$	$T_{VJ} = 25^{\circ}C$				20	μA		
		$V_R = 0.8 \cdot V_{RRM}$	$T_{VJ} = 150^{\circ}C$			1	1.5	mΑ		
V _F		$I_F = 50$ A	$T_{VJ} = 25^{\circ}C$				1.30	V		
			$T_{VJ} = 150^{\circ}C$		-		1.37	V		
V _{F0}		For power-los	s calculations o	nly			0.89	V		
r _F		$T_{VJ} = 150$ °C				5	9.8	$m\Omega$		
T _{VJ}					-40		150	°C		
I _{F(AV)}	*	$T_c = 100$ °C	180° rect.	$T_{VJ} = 150$ °C		31		Α		
I _{FSM}	*	$T_{VJ} = 45^{\circ}C$	t = 10 ms (50) Hz, sine			320	Α		
		$V_R = 0 V$	t = 8.3 ms (60))) Hz, sine			350	Α		
		$T_{VJ} = 150$ °C	t = 10 ms (50)) Hz, sine			270	Α		
		$V_R = 0 V$	t = 8.3 ms (60))) Hz, sine	A ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~		290	Α		
l² t	*	T _{VJ} = 45°C	t = 10 ms (50) Hz, sine	45		510	A s ²		
		$V_R = 0 V$	t = 8.3 ms (60))) Hz, sine			510	A s		
		T _{VJ} = 150°C	t = 10 ms (50) Hz, sine	-//		360	A s ²		
		$V_R = 0 V$	t = 8.3 ms (60)) Hz, sine			350	A s ²		
RthJC	*	DC current					1.10	K/W		

^{*} Data according to assembled Chip

VHFD (bondable)

Data according to IEC 60747

Terms of Conditions and Usage

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Should you intend to use the product in aviation applications, in health or life endangering or life support applications, please notify. For any such applications we urgently recommend

- to perform joint risk and quality assessments;
- the conclusion of quality agreements;
- to establish joint measures to ensure application specific product capabilities and notify that IXYS may delivery dependent on the realization of any such measures.